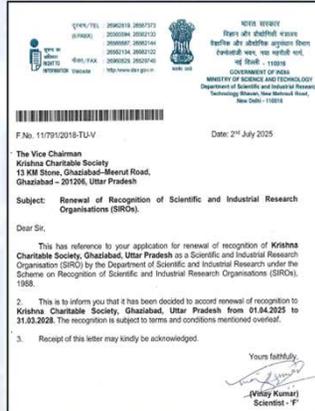## DEPARTMENTAL HIGHLIGHTS

Scientific and Industrial Research Organizations (SIROs) Recognition

KIET University, Delhi-NCR, Ghaziabad (UP), under Krishna Charitable Society recognized as a Scientific and Industrial Research Organisations (SIROs) by the Department of Scientific and Industrial Research (DSIR), Ministry of Science and Technology, Government of India. This prestigious recognition help KIET to establish in house Research and Development on new technologies, products, processes etc. It also provides a link between scientific laboratories and industrial establishments for transfer of technologies through National Research Development Corporation (NRDC) and facilitates investment in R&D through Central Electronics Limited (CEL).

Centre of Advanced VLSI Design (Powered by Cadence University Program)

The Centre of Advanced VLSI Design (Powered by Cadence University Program) at KIET University, Ghaziabad is a state-of-the-art facility dedicated to fostering innovation, research, and excellence in VLSI design. The centre has 30 computer system with advanced configuration and is equipped with the **Cadence University Research Bundle.** The Cadence University Research bundle consists of all the Front-end and Back-end tools required for chip design. The objectives of Centre of Advanced VLSI Design is to bridge the gap between academic learning and industry requirements by providing industry standard EDA tools and training in VLSI design, simulation, verification, and implementation.

Robotics and Mechatronics Lab

Center of Excellence in Robotics and Mechatronics has been established by Department of ECE, KIET University. This specialized lab operates beyond curriculum and provides interdisciplinary hands-on learning opportunities for students across different domains. It houses advanced robotic platforms including Fire Bird V, VI, XII, 4-wheel mobile robots, and a **humanoid robot named Anushka** (in picture). Students from the Center actively participate in national-level robotics initiatives such as e-Yantra competitions supported by IIT Bombay, gaining experiential exposure, robotics kits, and notable achievements.

CONFERENCE SECRETARIAT

Chief-Patron

Shri Sarish Agarwal
Hon'ble Chairman
KIET University

Shri Atul Garg
Chancellor,
KIET University

Shri Sunil P. Gupta
General Secretary,
KIET University

Patron

Dr. Manoj Goel
Executive Director
KIET University

Dr. Adesh Kr. Pandey
Director Academics
KIET University

Dr. Abhinav Juneja
Director CRPC
KIET University

Honorary Chair

Dr. S.N. Singh
Advisor, IEEE-UP Section
Dr. Yogesh S. Chauhan
Section Chair, IEEE-UP Section
Dr. Md. Rihan
Director General, NISE, Delhi
Dr. Satish K. Singh
Immediate Past Section Chair, IEEE-UP Section
Prof. Asheesh Kumar
Award & Recognition, IEEE-UP Section

Dr. R. K. Singh,
Professor, Dept. of Electrical Engineering, IIT BHU
Dr. Prabhakar Tiwari
Student Activities, IEEE-UP Section
Dr. Varun Kakar
Joint Secretary, IEEE-UP Section
Dr. Arun Kumar Singh
Dean Academics, REC, Kannauj
Dr. Rafik Ahmad, Vice Chair, Sight, IEEE UP Section
Dr. Pavan Kr. Shukla,
Sight Chair, IEEE UP Section

General Chair

Dr. Vibhav Kumar Sachan
Dean (R&D) & Dean (ECE)
KIET University

Conference Chair

Dr. Parvin Kumar
Professor, Dept. of ECE, KIET University

Dr. Ruchita Gautam
Professor, Dept of ECE, KIET University

Mr. Sachin Tyagi
Asst. Professor Dept. of ECE, KIET University

Technical Chair

Dr. Abhishek Sharma

Dr. Praveen Kumar

Dr. Urvashi Gupta

Publication Chair

Dr. Parvin Kumar
Dr. Vidyadhar Gupta

Dr. Vibhu Srivastava
Dr. Mukesh Kr, Tripathi

Dr. Puneet Garg
Dr. Manish Rai

Printing Chair
Dr. Richa Srivastava
Ms. Shraddha Sood

Website & Publicity
Dr. Anil Kumar
Ms. Ragini Sharma

Finance Chair
Mr. Mohit Tyagi
Dr. Balram Tamrakar

KIET UNIVERSITY
DELHI-NCR, INDIA
(Under Section 3 of the UGC Act, 1956)

IEEE UP SECTION (INDIA)

IEEE ELECTRONICS PACKAGING SOCIETY

6th International Conference

on

Emerging VLSI and Semiconductor Technology for AI and Computing Applications

(EVST- 2026)

Conference Record #69093

26th -27th June 2026

Organized by

Department of Electronics and Communication Engineering
KIET University, Delhi-NCR, Ghaziabad

(Recognized as a Scientific & Industrial Research Organisations (SIROs) by Department of Scientific and Industrial Research (DSIR), New Delhi and Rank Band (151-200) for Engineering in NIRF 2025.)

ABOUT KIET

Established in 1998 with an initial intake of 180 students, KIET has grown into a premier institution recognized for academic excellence and industry-aligned education. With 27 years of legacy in Engineering, Pharmacy, Management, and Computer Applications, KIET has continually strengthened its academic standards and infrastructure. **The Institute achieved Autonomous Status in 2024 and has been conferred Deemed-to-be-University status under Section 3 of the UGC Act, 1956 in November 2025.** The Institute now has 9300+ students and is empowered with 440+ highly qualified full-time faculty to nurture our students, with 100% Placement & Internship assistance. Consistency in maintaining academic results is one of the key strengths of KIET.

The Institute is accredited with NAAC 'A+', and all eligible programs are NBA accredited. In the **NIRF India Rankings 2025, KIET secured Rank 71 in Pharmacy, 151–200 band in Engineering, and 11–50 band in Innovation.** The Institute also boasts a **QS I-GAUGE Diamond rating** and recognition as **a Scientific and Industrial Research Organization (SIRO) by DSIR.** Our institute harbors in-house Entrepreneurship Cell, i.e. **Technology Business Incubator (TBI-KIET),** Innovation Centre, Department of Public & International Relations, and a dedicated Placement Center (CRPC) that upskill and hone the talent of students, through various activities. KIET gives equal importance to curricular, co-curricular and extra-curricular activities. KIET regularly organizes a plethora of such activities to give a maximum opportunity to its students to groom into confident and strong personalities - enrich their learning experiences. KIET houses a robust ecosystem of Centers of Excellence, serving as catalysts for advanced learning, research, and innovation. These include **Supercomputing Centre of Excellence, Centre for Space Technologies, Advanced Woodworking Centre, Centre for Drone Technologies, Herbal Product Development Centre, Centre for E-Mobility Solutions, Wipro Centre of Excellence in Cyber security, Centre for Automotive Mechatronics, Centre for Advanced VLSI Design** etc.

ABOUT THE DEPARTMENT

Department of ECE grooms the students to excel in the field of technology. Our students are trained in both software and hardware skills and basic inputs are provided to make them self-confident to work in industry and get encouragement for higher studies and research. The Department's motto is to upgrade skills and knowledge of our young engineers to enable them not only to survive but be the best in the present competitive world. The department also contributes to the society by accomplishing technical projects that cater to the various requirements of the present-day world. The students are also encouraged to participate in various technical and extracurricular events. The department has qualified and dedicated faculty members to provide good technical support to all the students. The department of ECE has a vision to become a centre of excellence in the field of Electronics and Communication Engineering. All our faculty and students are dedicated to achieve this goal with full vigor, enthusiasm and good ethical values. The Department is involved in high quality research on several domains like Optical Integrated Circuits, Signal Processing and Communication, Semiconductor Device Characterization and Integration, Advanced Microwave Techniques and other emerging fields in Electronics and Communication Engineering. To foster and enrich the same, **department has signed MoUs with various industries such as Nokia, ISRO, TI, etc.**

ABOUT THE CONFERENCE (EVST-2026)

The **International Conference on Emerging VLSI and Semiconductor Technology for AI and Computing Applications 2026** is poised to be a significant gathering for professionals in the fields of **VLSI design, semiconductor technology, artificial intelligence, and computing systems.** The conference consists of various sessions, including keynotes and parallel sessions. Each session will be addressed by outstanding experts who will highlight recent advances in various facets of Emerging VLSI and Semiconductor Technology. The conference will offer the budding researchers with an opportunity to present their work in front of eminent experts in their respective fields. Discussions on the latest innovations, trends, practical concerns and challenges faced in these fields will take place. Innovation will bring added value and widen the employment base. The present conference aims to establish robust, mutual interactions and cooperation with scientific educational, doctors, governmental, non-governmental organizations, industries across local, national and international levels. Its objective is to create as many meaningful connections as possible to enable scientific and industrial exchange, to strengthen research collaborations with scientific centers across the country and around the world.

PARTICIPATION

The Conference is aimed to felicitating the exchange of ideas and concepts among a broad group that includes academicians, students, researchers, innovators and industry.

CONFERENCE TRACKS

Category-1: Emerging Materials and Devices Technologies

- 2-D Material
- High-voltage MOSFET
- Si photonics and optoelectronics Spintronics and quantum materials
- MEMS/NEMS
- Organic electronics
- Emerging memory technologies.

Category-2: IoT & Embedded Systems Design

- Generative AI Applications in Embedded and Machine Learning
- Real-Time Systems and Applications
- Embedded Software and Firmware Development Hardware/Software Co-Design
- IoT, Edge, and Wireless Embedded Systems
- Embedded Vision and Multimedia Processing
- Reconfigurable Systems and FPGAs
- Automotive, Healthcare, and Industrial Applications
- Advanced Robotics

Category-3: VLSI Circuit and SoC Design

- Digital, Analog, Mixed signal, RF circuits
- RTL design
- Processor Architecture
- Hardware accelerators
- FPGA based design
- Quantum circuits
- Neuromorphic
- Memory
- Circuits, Accelerators, SoC for ML/AI applications

Category-4: Packaging and Manufacturing

- Manufacturing technologies
- Packaging
- Signal and Power Integrity

Category-5: Electronics Design Automation, Testing and Verification

- CAD Tools, software
- CAD for FPGA
- High Level Synthesis
- System software
- Testing and Verification

Category-6: Role of AI/ML in Emerging Technologies

- Industry 5.0 – Human-Machine Collaboration
- Quantum Computing and Communication
- 6G Wireless Systems & NGN
- Green Technologies based on SDGs

GUIDELINES FOR AUTHORS

Soft copy of the full length paper in MS word/pdf format (as per the conference template) must be submitted before **28ᵗʰ February 2026.** All the submitted full-length papers will be reviewed and evaluated based on the originality, technical aspects, research depth, authenticity of the data used and relevance to the theme of the conference. The work should be original and must not have been published in any Journal, conference proceedings or any other platforms. **All submissions must adhere to a plagiarism limit of less than 20%.**

All authors are required to submit unpublished full-length research papers to Microsoft CMT platform:

https://cmt3.research.microsoft.com/EVST2026

PUBLICATION AND INDEXING

Accepted and presented papers will be submitted for inclusion in the IEEE Xplore digital library, subject to meeting IEEE Xplore's scope and quality requirements.

IMPORTANT DATES

Paper Submission deadline	Feb 28, 2026 (IST) March 15, 2026 (IST)
Notification of Acceptance	March 30, 2026 (IST)
Camera-ready paper submission & Registration deadline	April 30, 2026 (IST)
IEEE EVST 2026 Conference Date	June 26-27, 2026 (IST)

REGISTRATION FEES

- Non-IEEE Professionals — INR 8500
- IEEE Professional Members — INR 7000
- Non-IEEE Students — INR 5500
- IEEE Student Members — INR 4500
- Industry Participants — INR 8000
- Non-IEEE Foreign Participants — USD 300
- IEEE Foreign Participants — USD 250

*Note

- ❑ All papers are required to be prepared and submitted in the IEEE Standard two – column conference format of A4 size in English. Papers must be four to six pages in length, including texts, Figures, photographs and references.
- ❑ Detailed format information is available in IEEE Website. https://www.ieee.org/conferences/publishing/templates

CONNECT WITH US

Dr. Parvin Kumar & Mr. Sachin Tyagi
(Conference Chair EVST-2026)

Phone: +91 8860548163
+91 9650858161
+91 8506097863
+91 9643142808

Website: https://www.kiet.edu/evst2026/
Email Id: icce@kiet.edu

MODE OF PAYMENT

Payment through NEFT/RTGS/IMPS or DD in favor of KIET Group of Institutions payable at GHAZIABAD.

Account Details	For National Delegates	For International Delegates
Name	KIET Group of Institutions	Krishna Institute of Engineering &Technology
A/C NO.	508011003720	125601000763
IFSC Code/SWIFT Code	KKBK0005295	ICICINBBNRI
Bank Name	Kotak Mahindra Bank	ICICI Bank Ltd.
Branch	Navyug Market, Ghaziabad201001	Ambedkar Road, Opp. Nehru YuvaKendra, Ghaziabad

INTERNATIONAL & NATIONAL ADVISORY BOARD

Prof. Neeta Pandey, Professor, DTU, Delhi
Dr. Manoj Kumar Tiwari, Senior Principal Design, Cadence Design System, Hyderabad
Prof. Poornima Mittal, Professor, DTU, Delhi
Dr. Rajeev Kumar Ranjan, Associate Professor, IIT(ISM), Dhanbad
Dr. Vandana Niranjan, HoD, ECE, IGDTUW, New Delhi
Dr. Robertas Damaševičius, Kaunas University of Technology, Lithuania
Alkinoos ATHANASIOU, Aristotle University of Thessaloniki, Thessaloniki, Greece
Dr. Ashish Khanna, Maharaja Agrasen Institute of Technology, GGSIP University, India
Dr. Deepak Gupta , Maharaja Agrasen Institute of Technology, GGSIP University, India
Mr. Pedro Pedrosa Rebouças Filho, Faculdade de Engenharia da Universidade do Porto
Dr. D. Jude Hemanth, Karunya University, India
Dr. Arun Kumar Sangaiah, Vellore Institute of Technology (VIT)
Dr. Roberto Munoz, Universidad de Valparaíso, Chile
Dr. Auzuir Ripardo de Alexandria, Federal Institute of Education, Science and Technology
Dr N Arunkumar, Sastra University, India
Dr. João Alexandre Lobo Marques, University of Saint Joseph, Macau, SAR, China
Mr. Khan Muhammad, Sejong University, Seoul, Republic of Korea
Dr. Sandeep Pirbhulal, Shenzhen Institutes of Advanced Technology, China
Mr. Qammer H. Abbasi, University of Glasgow, Glasgow, UK
Dr. Mohamed Abdel-Basset, Zagazig University, Sharqiyah, Egypt
Dr. Ali Hassan Sodhro, Linkoping University, Linkoping
Dr. Gunasekaran Manogaran, University of California Davis, USA
Dr. María de los Ángeles Quezada , Department of Informatics Engineering Instituto Tecnológico de Tijuana
Dr. Giancarlo Fortino, SM IEEE, University of Calabria, Italy
Dr. Javier Del Ser Lorente, SM IEEE University of the Basque Country, Bilbao, Spain
Dr. Joel J.P.C. Rodrigues, SM IEEE Instituto de Telecomunicações, 6201-001 Covilhã, Portugal
Mr. Antonio Celesti, PhD Università degli Studi di Messina, Italy
Mr. Wanqing Wu, Sun Yat-Sen University, Guanzhou, China
Mr. Muhammad Imran, King Saud University, Saudi Arabia
Mr. Prayag Tiwari, University of Padova, Italy
Dr. Aditya Khamparia, Lovely Professional University, Punjab, India
Dr. Le Hoang Son, Vietnam National University
Dr. Heye Zhang, Sun Yat-Sen University, Guangzhou, China
Dr. Mukesh Prasad, University of Technology Sydney
Dr. Tanveer Hussain, Sejong University, Seoul, Republic of Korea
Mr. Giovanna Sannino, ICAR, National Research Council of Italy, Naples, Italy
Mr. Zhifan Gao, Western University, Canada
Dr. Marcin Woźniak, Silesian University of Technology, Poland
Mr. Fath u min ullah, ejong university, seoul, south korea
Mr. Yuhui Zheng, School of Computer and Software, Nanjing, Jiangsu China

Dr. K. P. Rajput, Research Associate SnT, University of Luxembourg
Dr. Chhagan Charan, Assistant Professor NIT Kurukshetra
Dr. Astik Biswas, Assistant Professor ORACLE
Dr. Sandeep Singh, Assistant Professor NIT Hamirpur
Dr. Sachin, Assistant Professor SRM University, Chennai
Dr. Madhu Jain, Associate professor ECE Deptt. (JP NOIDA)
Dr Bhawna Aggarwal, Associate professor ECE Deptt. Netaji Subhas University of Technology
Dr. Payal Gupta, Luleå University of Technology Sweden
Dr. Suman Deb, SVNIT Surat
Dr. Ghanapriya Singh, NIT Kurukshetra
Dr. Kamini Upadhyay, ZS Associates
Dr. Harikesh, DTU Delhi
Dr Ajay Kumar, EMLYON Business School, France
Dr Manoj Tiwari, ST Microelectronics, Gautam Buddha Nagar, Uttar Pradesh, India
Dr Pankaj Verma, NIT Kurukshetra
Dr Anirbhan Bhowmick, VIT Bhopal
Prof Umesh Ghanekar , Professor NIT Kurukshetra
Prof RP Tewari , Professor MNNIT Allahabad
Prof NK Shukla , University of Allahabad
Dr Tanmoy Hazra , Assistant Professor IIIT Pune
Dr Rahul Dixit , Assistant Professor IIIT Pune
Prof Ritu Tiwari , Professor IIIT Pune
Prof RC Singh , Professor DTU
Prof Naveen Kumar, Professor DTU
Dr Promod Yadav , Assistant Professor NIT Srinagar
Prof Neeraj Khare , Professor IIT Delhi
Mr Subhash Yadav , Associate Director Cognizant
Mr Anurag Tiwari, Manager IFFCO
Dr Sundeep Rohilla, CEO MBS India Pvt Ltd
Mr IN Kishore, Director Learning NOKIA
Mr Vimal Singh , Joint Director MIeTY
Dr. Laxya singla, Maharaja Agrasen Institute of Technology, Delhi
Dr Abhimanyu Singh, GJUST, Haryana
Dr Shelly Singla, MIT, Moradabad
Dr Bajrang Bansal, JIIT, Noida
Dr Srikant Satya, SRM, Modi Nagar
Dr Saptarishi, SRM, Modi Nagar
Dr Nidhi Goel, IGDTU, New Delhi
Dr Sarika Singh, NIT Jamshedpur
Dr. Muhammad Fazal Ijaz, Department of Intelligent Mechatronics Engineering, Sejong University, Seoul 05006, South Korea
Prof. Victor Hugo C. de Albuquerque, Department of Teleinformatics Engineering (DETI) Federal University of Ceará, BraziL

KEYNOTE SPEAKERS

Dr Rajendra Pratap, Vice President, HCLTech, Noida
Dr. Manoj Kumar Tiwari, Senior Principal Design, Cadence Design System, Hyderabad
Dr Neelkanth Kundu, Assistant Professor, IIT Delhi
Dr. Poornima Mittal, Professor, Delhi Technological University, India
Dr. Vandana Niranjan, HoD & Professor, Indira Gandhi Delhi Technical University for Women, New Delhi, India
Dr Shyam Lal Assistant Professor, NIT, Suratkal
Dr. Jayashree H.V., Manager, Engineering Development, Qualcomm India Pvt. Ltd.
Prof. Neeta Pandey, Professor, DTU, Delhi
Dr. Rajeev Kumar Ranjan, Associate Professor, IIT(ISM), Dhanbad
Dr. Sunny, Associate Professor, Indian Institute of Information Technology-Allahabad, Prayagraj

BEST PAPER AWARDS

The "Best Paper Awards" will be given for each track of conference, selected by the committee among the presented papers in EVST 2026.
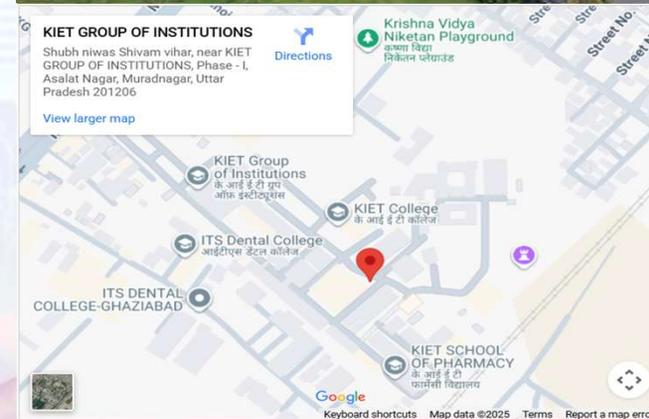
1st Best Paper Award — **Rs. 4,000/- Cash Prize with goodies**

2nd Best Paper Award — **Rs. 2,000/- Cash Prize with goodies**

Location Map (KIET)

KIET GROUP OF INSTITUTIONS
Shubh niwas Shivam vihar, near KIET GROUP OF INSTITUTIONS, Phase - I, Asalat Nagar, Muradnagar, Uttar Pradesh 201206

View larger map

https://maps.app.goo.gl/ickxqdSHiXHKWW13A